



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

University College of Engineering (Autonomous), Osmania University, Hyderabad - 500 007

Academic Year 2015 - 2016

B.E.II/IV (ECE) –I Semester

TIME TABLE

W.E .F: 13th July 2015

Room No: B.E II (ECE) Class Room 118

| Days | 9.00AM to 10 AM | 10 Am to 11 AM | 11 AM to 12 PM | 12 PM to 1 PM | 2 PM to 3 PM | 3 PM to 4 PM | 4 PM to 5 PM |
|-----------|-----------------|----------------|----------------|---------------|--------------|--------------|--------------|
| Monday | PTSP | M-III | SD | CA | BEE | MEA | *** |
| Tuesday | *** | M-III | PTSP | BEE | SD | SD/EE LAB | |
| Wednesday | M-III | BEE | PTSP | CA | SD | SD/EE LAB | |
| Thursday | MEA | SD | PTSP | CA | BEE | *** | *** |
| Friday | CA | M-III | SD/EE LAB | | MEA | MEA | *** |

Key to Subjects

Theory:

1. EC 201 UE- Semiconductor Devices (SD)
2. EC 202 UE- Circuit Analysis (CA)
3. EC 203 UE- Probability Theory and Stochastic Processes (PTSP)
4. CM 221UE- Managerial Economics and Accountancy (MEA)
5. EE 222 UE- Basic Electrical Engineering (BEE)
6. MT 201UE- Mathematics- III (M-III)

Practicals:

1. EC 231 UE- Semiconductor Devices Lab (SD Lab)
2. EE 241 UE- Electrical Engineering Lab (EE Lab)

List of Faculty

1. Mrs.R.Sandhya.
2. Mr.R.Narender Reddy
3. Mrs.P.Srividya
4. Mrs.C.Vani
5. Mr. S. Vijender Reddy.
6. Dr. P. Thirupathi.and Mr. K. Ramalingiah.

1. Mrs.P.Padma and Mrs.R.Sandhya.
2. S. Vijender & G. Srikanth.

ACADEMIC CALENDER

Commencement of Instruction: 13th July 2015

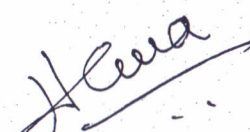
First Class Tests: 3– 5th Sept 2015

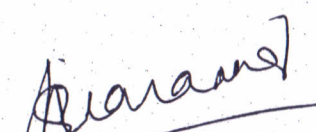
Second Class Tests: 2 -4th Nov 2015


Last date of Instruction: 7th Nov 2015

Preparation Holidays & Practical Examinations: 9 -21st Nov 2015

Theory Examinations: 23rd Nov – 14th Dec 2015


Dr.R.Hemalatha
Staff Member Incharge


Mrs.Prasanna SVSNDL
Timetable Coordinator 2015-16


Dr.B. Rajendra Naik
Head, Dept.of ECE


Prof.S.Ramachandram
Principal, UCE, OU



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

University College of Engineering (Autonomous), Osmania University, Hyderabad - 500 007

Academic Year 2015 - 2016

B.E.III/IV (ECE) – I Semester

TIME TABLE

W.E .F: 13th July 2015

Room No: B.E III (ECE) Class Room

| Days | 9 AM to 10AM | 10 AM to 11 AM | 11 AM to 12 PM | 12 PM to 1 PM | 2 PM to 3 PM | 3 PM to 4 PM | 4 PM to 5 PM |
|-----------|--------------|----------------|----------------|---------------|--------------|--------------|--------------|
| Monday | AC | PDC | DICA | LICA | TLA | PDC/IC LAB | |
| Tuesday | CS | AC | TLA | DICA | PDC | PDC/IC LAB | |
| Wednesday | PDC | CS | TLA | LICA | PDC/IC LAB | | *** |
| Thursday | LICA | PDC | DICA | CS | AC | *** | *** |
| Friday | TLA | CS | AC | DICA | LICA | *** | *** |

Key to Subjects

Theory:

- 1.EC 301 UE- Analog Communications (AC)
2. EC 302 UE- Pulse and Digital circuits (PDC)
3. EC 303 UE- Linear Integrated Circuit Applications (LICA)
- 4.EC 304UE- Digital Integrated Circuit Applications (DICA)
5. EC 305 UE- Control Systems (CS)
- 6.EC 306 UE- Transmission Lines and Antennas (TLA)

Practicals:

1. EC 331 UE- Pulse and Digital Circuits Lab (PDC Lab)
- 2.EE 332 UE- Integrated Circuits Lab (IC Lab)

List of Faculty

1. Dr. L. Nirmala Devi.
2. Mr.M.Shyam Sunder.
3. Mr. Md.Misbahuddin
4. Mrs. P. Kotilakshmi.
5. Mr. B. Ramesh.
6. Mrs.A.Bharathi.

1. Dr.R.Hemalatha&Mr. M.Shyam Sunder
2. Dr.B.Rajendra Naik&Mr.Md.Misbahuddin

ACADEMIC CALENDER

Commencement of Instruction: 13th July 2015

First Class Tests: 3– 5th Sept 2015

Second Class Tests: 2 -4th Nov 2015

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
University College of Engineering (Autonomous), Osmania University, Hyderabad - 500 007
Academic year 2015 -16

B.E.IV/IV (ECE) -I Semester Room No.102

REVISED TIME TABLE

W.E.F: 13th July, 2015

| Days | 9.00 to 10.00AM | 10.00AM to 11.00AM | 11 Am to 12PM | 12PM to 1PM | 2PM to 3PM | 3Pm to 4PM | 4PM to 5PM |
|-----------|-----------------|--------------------|---------------|-------------|-------------------|------------|------------|
| Monday | COA/SC | MWT | ESD | DCCN | Project Seminar * | | *** |
| Tuesday | VLSID | MWT | ESD | COA/SC | Project Seminar * | | *** |
| Wednesday | VLSID | ESD | DCCN | COA/SC | EDA/MWLAB | | *** |
| Thursday | MWT | VLSID | DCCN | ESD | EDA/MWLAB | | *** |
| Friday | DCCN | MWT | COA/SC | VLSID | EDA/MWLAB | | *** |

Key to Subjects Theory:

1. EC 401UE: Embedded System Design (ESD)
 2. EC 402 UE: VLSI Design (VLSID)
 3. EC 403UE : Microwave Techniques (MWT)
 4. EC: 404UE: Data Communication and Computer Networks
 5. Elective II : EC 406UE Computer Organization and Architecture
 - EC 408 UE Satellite communication
- Practicals: EC 431 UE: Microwave Lab(MWL)
EC 432 UE: Electronic Design and Automation lab (EDA)
EC 433 UE: Project Seminar (PS)
SI 400 UE: Summer Internship (SI)

* Spoken Tutorial S/W AS E – Resource for Learning

* LATEX

** OSCAD @ FREE EDA

List of Faculty

1. Dr. R. Hemalatha
2. Dr. B. Rajendra Naik
3. Dr. D. Ramakrishna
4. Mrs. P. Padma
5. Dr. P. Chandra Sekhar
Dr. P. Naveen Kumar
6. Dr. D. Ramakrishna/Mrs. A. Bharathi
7. Dr. P. Chandra Sekhar/Mrs. P. Kotilakshmi
8. Dr. L. Nirmala Devi/Dr.P. Naveen Kumar

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Commencement of Instruction: 13th July 2015

First class test: 3 -5th Sept 2015

Second class test: 2 – 4th Nov 2015

Last date of Instruction: 7th Nov 2015

Practical Exams: 9 -21st Nov 2015

Theory Exams: 23rd Nov - 14th Dec 2015.

Dr. K. Hemalatha
Staff Member Incharge

Mrs. P. Padma S.V.SNDL
Timetable Coordinator 2011-12

Dr. B. Rajendra Naik
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Prof. S. Ramachandram
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